

40V, 135A, 2.8mΩ Dual N-channel Power SGT MOSFET

JMSH0404PGHWQ

Features

- Ultra-low ON-resistance, RDS(ON)
- Low Gate Charge
- 100% UIS Tested
- 100% ΔVds Tested
- Halogen-free; RoHS-compliant
- AEC-Q101 Qualified

Applications

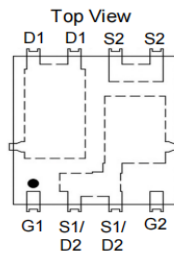
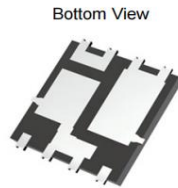
- Load Switch
- PWM Application
- General Automotive Application

Product Summary

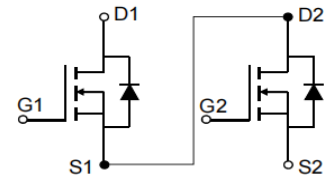
Parameters	Value	Unit
V_{DSS}	40	V
$V_{GS(th_Typ)}$	2.7	V
$I_D(@V_{GS}=10V)$	135	A
$R_{DS(ON_Typ)}(@V_{GS}=10V)$	2.8	mΩ



PDFN5X6-8L-HW



Pin Assignment



Schematic Diagram

Ordering Information

Device	Marking	MSL	Form	Package	Reel(pcs)	Per Carton (pcs)
JMSH0404PGHWQ-13	H0404PHQ	1	Tape&Reel	PDFN5x6-8L-HW	5000	50000

Absolute Maximum Ratings (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-to-Source Voltage	40	V
V_{GS}	Gate-to-Source Voltage	±20	V
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	135
		$T_C = 100^\circ\text{C}$	95
I_{DM}	Pulsed Drain Current ⁽¹⁾	Refer to Fig.4	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	212	mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	120
		$T_C = 100^\circ\text{C}$	60
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 175	°C

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽³⁾	44	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.3	

**Electrical Characteristics** ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 32\text{V}$, $V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	1.9	2.7	3.5	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽⁴⁾	$V_{GS} = 10\text{V}$, $I_D = 20\text{A}$	-	2.8	3.7	m Ω
Dynamic Characteristics						
R_g	Gate Resistance	$f = 1\text{MHz}$	-	1.7	-	Ω
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$, $V_{DS} = 20\text{V}$, $f = 1\text{MHz}$	1238	1733	2427	pF
C_{oss}	Output Capacitance		687	962	1346	pF
C_{rss}	Reverse Transfer Capacitance		41	58	81	pF
Q_g	Total Gate Charge	$V_{GS} = 0$ to 10V $V_{DS} = 20\text{V}$, $I_D = 20\text{A}$	19	27	37	nC
Q_{gs}	Gate Source Charge		-	8.9	-	nC
Q_{gd}	Gate Drain ("Miller") Charge		-	6.5	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}$, $V_{DD} = 20\text{V}$ $I_D = 20\text{A}$, $R_{GEN} = 3\Omega$	-	11	-	ns
t_r	Turn-On Rise Time		-	24	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	21	-	ns
t_f	Turn-Off Fall Time		-	8	-	ns
Body Diode Characteristics						
I_S	Maximum Continuous Body Diode Forward Current		-	-	135	A
I_{SM}	Maximum Pulsed Body Diode Forward Current		-	-	540	A
V_{SD}	Body Diode Forward Voltage	$V_{GS} = 0\text{V}$, $I_S = 20\text{A}$	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$I_F = 20\text{A}$, $di/dt = 100\text{A}/\mu\text{s}$	25	34	48	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	25	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 20\text{V}$, $V_{GS} = 10\text{V}$, $R_G = 25\text{ohm}$, $L = 3\text{mH}$, $I_{AS} = 11.9\text{A}$, $V_{DD} = 0\text{V}$ during time in avalanche.
 3. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB.
 4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.



Typical Performance Characteristics

Figure 1: Power De-rating



Figure 2: Current De-rating

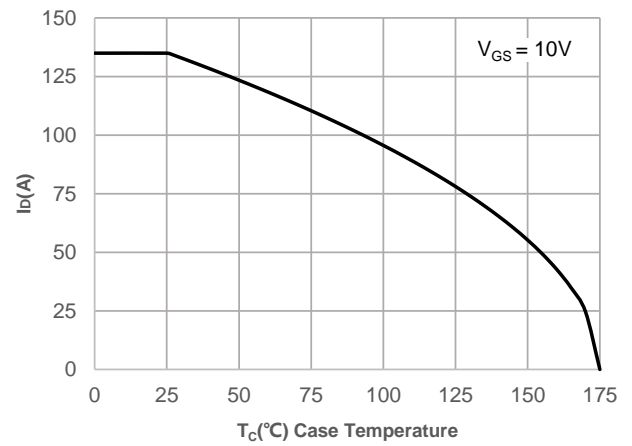


Figure 3: Normalized Maximum Transient Thermal Impedance

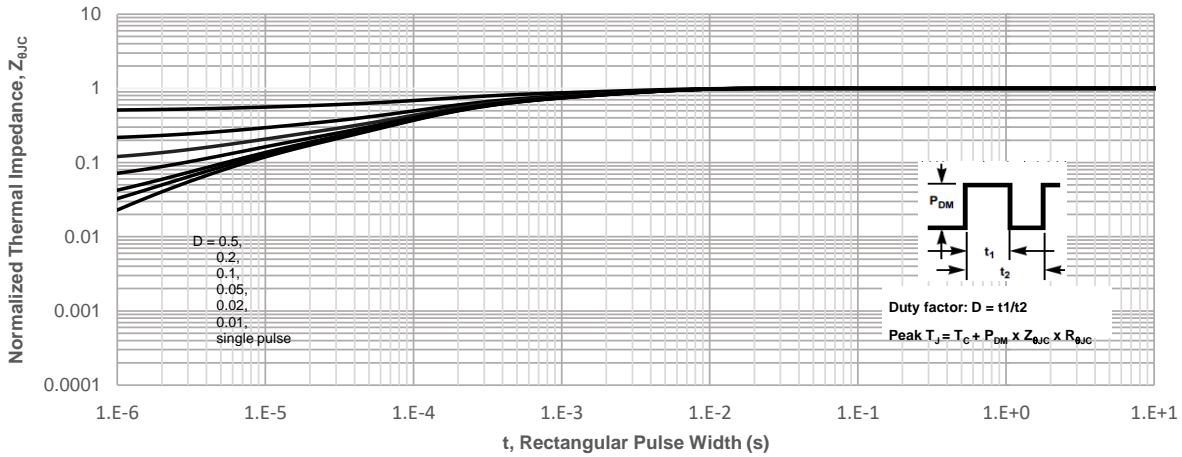
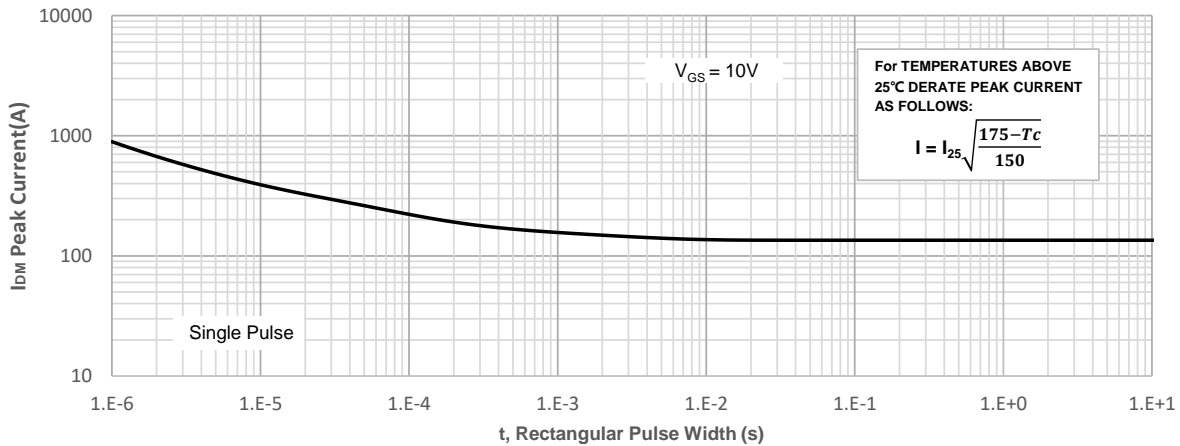


Figure 4: Peak Current Capacity



Typical Performance Characteristics

Figure 5: Output Characteristics

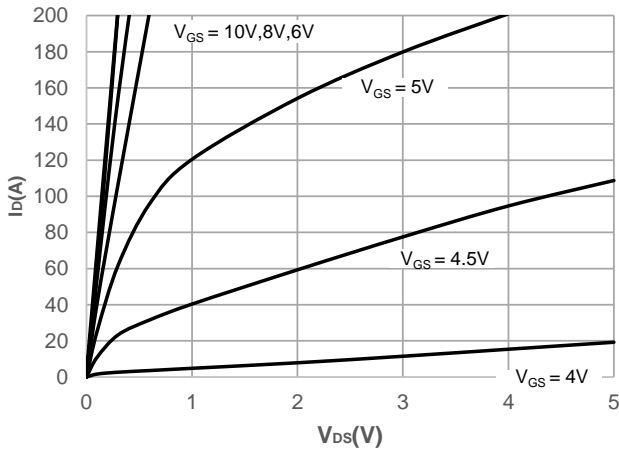


Figure 6: Typical Transfer Characteristics

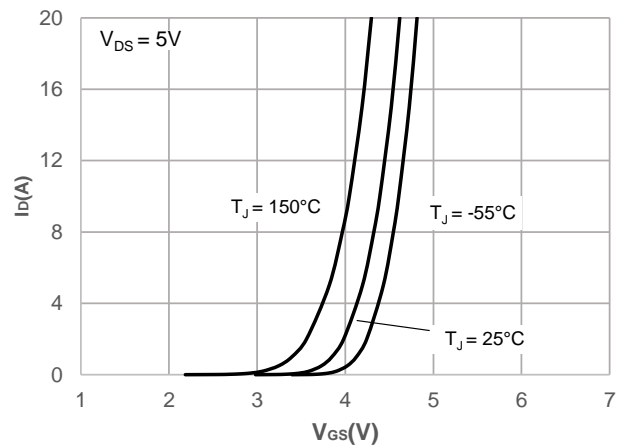


Figure 7: On-resistance vs. Drain Current

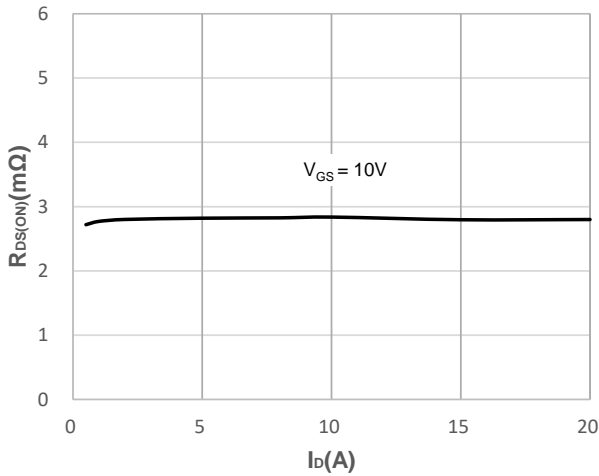


Figure 8: Body Diode Characteristics

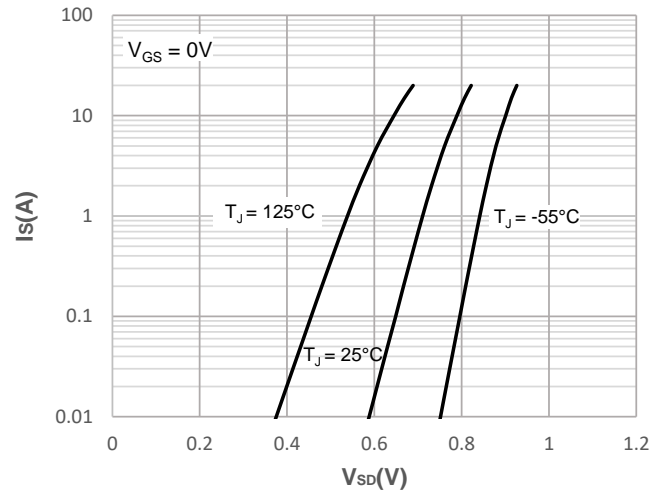


Figure 9: Gate Charge Characteristics

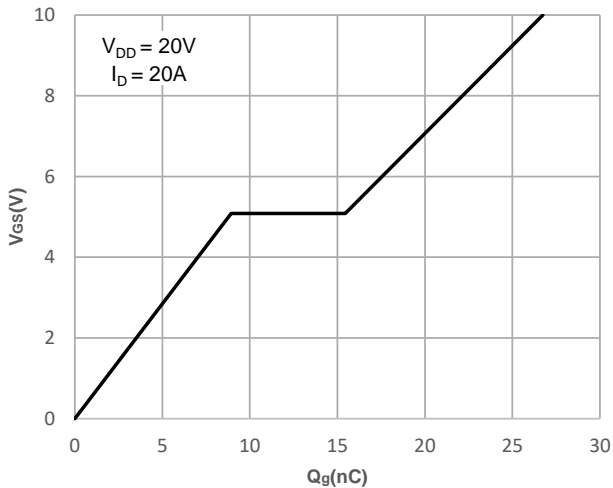
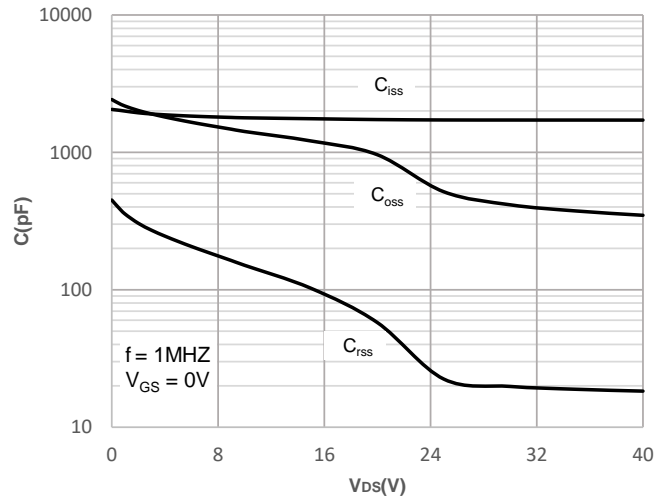


Figure 10: Capacitance Characteristics



Typical Performance Characteristics

Figure 11: Normalized Breakdown voltage vs. Junction Temperature

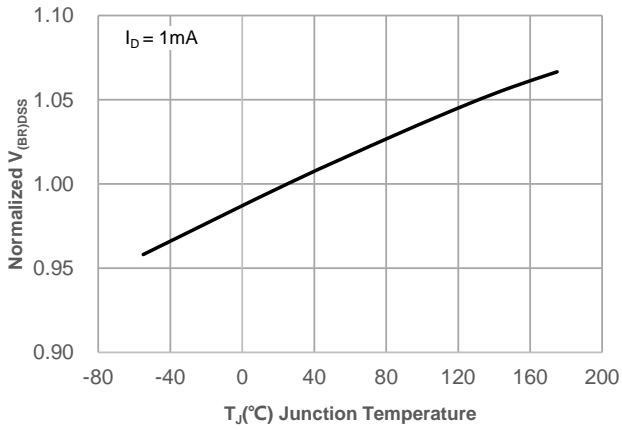


Figure 12: Normalized on Resistance vs. Junction Temperature

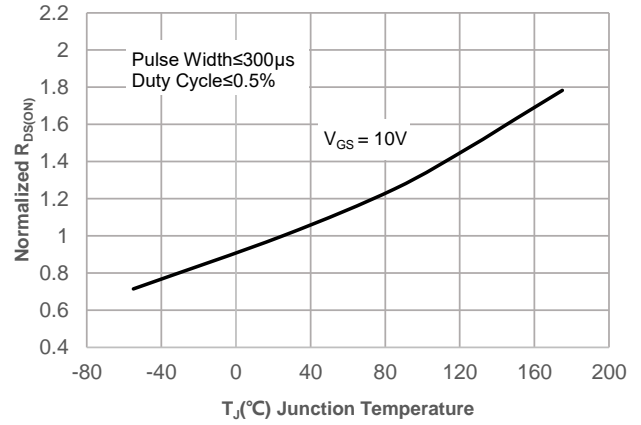


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

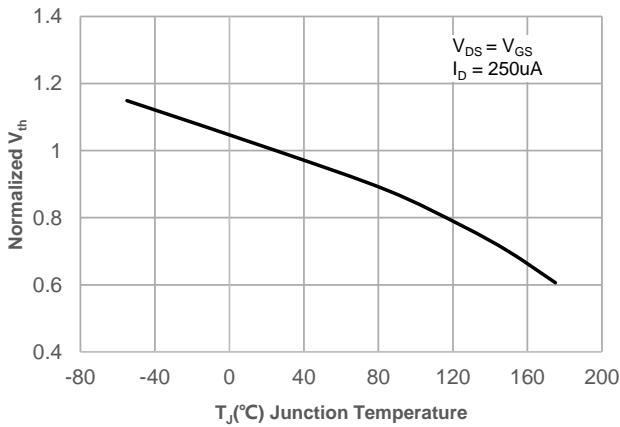


Figure 14: $R_{DS(ON)}$ vs. V_{GS}

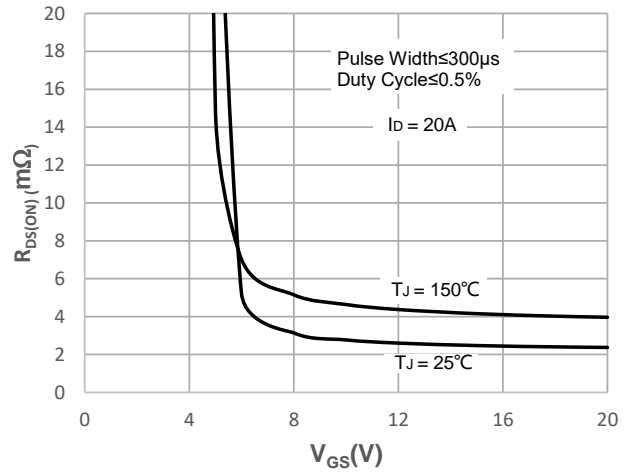
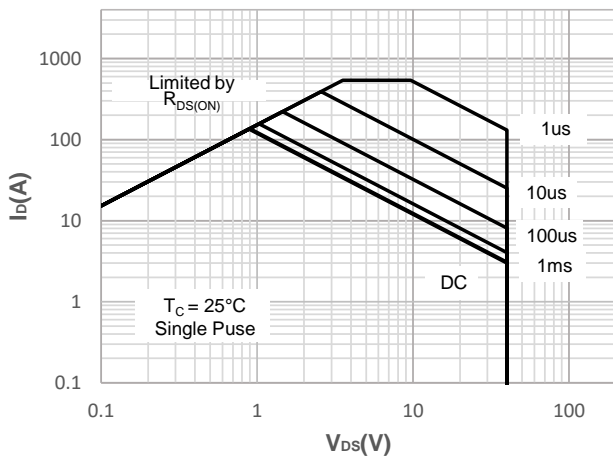


Figure 15: Maximum Safe Operating Area



Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveform

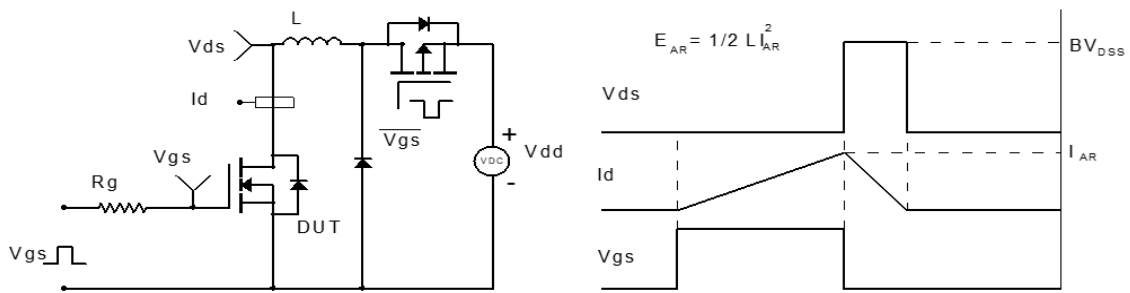


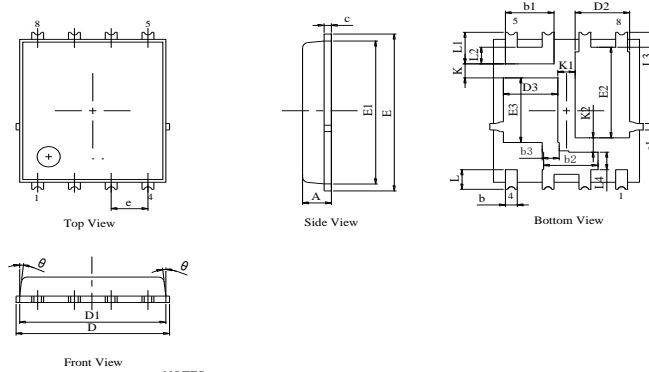
Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Figure 4: Diode Recovery Test Circuit & Waveform

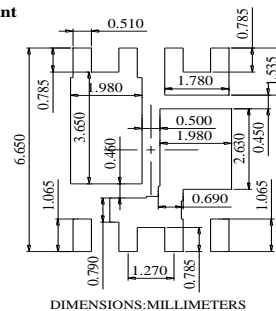


Package Mechanical Data(PDFN5X6-8L-HW)

Package Outline


- NOTES:
1. Dimension and tolerance per ASME Y14.5M, 1994.
 2. All dimensions in millimeter (angle in degree).
 3. Dimensions D1 and E1 do not include mold flash protrusions or gate burrs.

DIM	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.15
A1	0.00	-	0.10
b	0.31	0.41	0.51
b1	0.15	0.25	0.35
c	0.24	0.32	0.40
DIM	4.95	5.05	5.15
D1	4.00	4.10	4.20
D2	0.50	0.60	0.70
E	6.05	6.15	6.25
E1	5.50	5.60	5.70
E2	3.31	3.41	3.51
e	1.27BSC		
H	0.60	0.70	0.80
L	0.50	0.70	0.80
L1	-	-	0.13
a	-	-	12°

Recommended Soldering Footprint


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