

# 80V 2.35mΩ N-Ch Power MOSFET

## Features

- Ultra-low  $R_{DS(ON)}$
- Low Gate Charge
- 100% UIS Tested, 100%  $R_g$  Tested
- Pb-free Lead Plating
- Halogen-free and RoHS-compliant

## Product Summary

Items	Typ.	Units
$V_{DS}$	80	V
$V_{GS(th)}$	2.8	V
$I_D$ (at $V_{GS} = 10V$ ) <sup>(1)</sup>	143	A
$R_{DS(ON)}$ (at $V_{GS} = 10V$ )	2.35	mΩ

## Applications

- Power Management in Telecom., Industrial Automation, CE
- Current Switching in DC/DC & AC/DC (SR) Sub-systems
- Motor Driving in Power Tool, E-vehicle, Robotics

PDFN5x6-8L

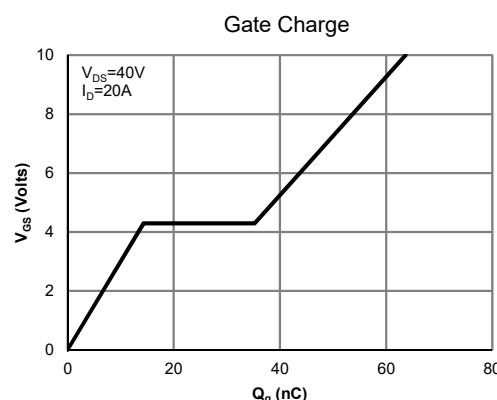
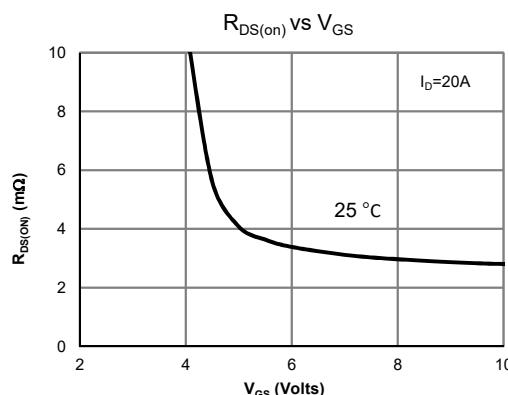


## Ordering information

Device	Package	Pins	Marking	MSL	$T_J$ (°C)	Reel Size	Quantity
JMSH0803AG-13	PDFN5x6-8L	8	H0803A	1	-55 to 150	13-inch Reel	3000

## Absolute Maximum Ratings $T_A=25^\circ C$ (unless otherwise specified)

Parameter	Symbol	Value	Units
Drain to source voltage	$V_{DS}$	80	V
Gate to source voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	143	A
$T_C=70^\circ C$		115	
Pulsed Drain Current <sup>(2)</sup>	$I_{DM}$	600	
Avalanche Current <sup>(3)</sup>	$I_{AS}$	60	A
Avalanche energy $L=0.1mH$ <sup>(3)</sup>	$E_{AS}$	180	mJ
Power Dissipation	$P_D$	104	W
$T_C=70^\circ C$		67	
Junction and Storage Temp. Range	$T_J, T_{STG}$	-55 to 150	°C



**Electrical Characteristics ( $T_J=25^\circ C$  unless otherwise specified)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
<b>STATIC PARAMETERS</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=250\mu A, V_{GS}=0V$	80			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=80V, V_{GS}=0V$ $T_J=55^\circ C$			1	$\mu A$
					5	
Gate-Body leakage current	$I_{GSS}$	$V_{DS}=0V, V_{GS} = \pm 20V$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	2.8	4	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$		2.35	2.8	$m\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=20A$		78		S
Diode Forward Voltage	$V_{SD}$	$I_S=1A, V_{GS}=0V$		0.7	1	V
Maximum Diode Continuous Current	$I_S$	$T_C=25^\circ C$			104	A

**DYNAMIC PARAMETERS<sup>(5)</sup>**

Input Capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=40V, f=1MHz$		4250		pF
Output Capacitance	$C_{oss}$			1340		pF
Reverse Transfer Capacitance	$C_{rss}$			8		pF
Gate resistance	$R_g$	$V_{GS}=0V, V_{DS}=0V, f=1MHz$		1.2		$\Omega$

**SWITCHING PARAMETERS<sup>(5)</sup>**

Total Gate Charge	$Q_g(10V)$	$V_{GS}=10V, V_{DS}=40V, I_D=20A$		63		nC
Total Gate Charge	$Q_g(6V)$			43		nC
Gate Source Charge	$Q_{gs}$			14		nC
Gate Drain Charge	$Q_{gd}$			20		nC
Turn-On DelayTime	$t_{D(on)}$	$V_{GS}=10V, V_{DS}=40V, R_L=2\Omega, R_{GEN}=6\Omega$		14		ns
Turn-On Rise Time	$t_r$			22		ns
Turn-Off DelayTime	$t_{D(off)}$			65		ns
Turn-Off Fall Time	$t_f$			37		ns
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F=20A, dI/dt=100A/\mu s$		65		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F=20A, dI/dt=100A/\mu s$		147		nC

**Thermal performance**

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient	$R_{\theta JA}$	50	65	$^\circ C/W$
Maximum Junction-to-Case	$R_{\theta JC}$	0.9	1.2	$^\circ C/W$

**Notes:**

1. Computed continuous current assumes the condition of  $T_{J\_Max}$  while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under  $T_{J\_Max} = 150^\circ C$ .
3. This single-pulse measurement was taken under the following condition [ $L = 100\mu H, V_{GS} = 10V, V_{DD} = 40V$ ] while its value is limited by  $T_{J\_Max} = 150^\circ C$ .
4. The power dissipation  $P_D$  is based on  $T_{J\_Max} = 150^\circ C$ .
5. This value is guaranteed by design hence it is not included in the production test.

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

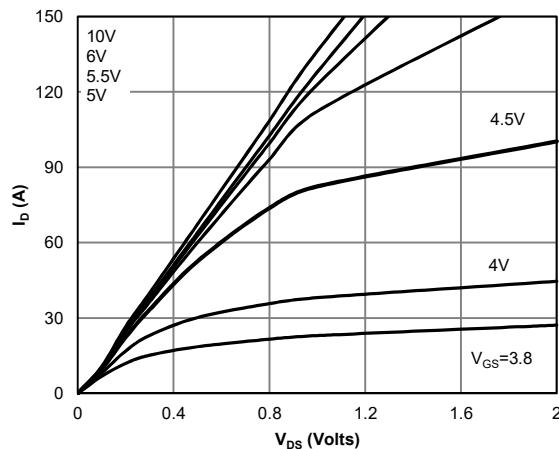


Fig 1: Saturation Characteristics

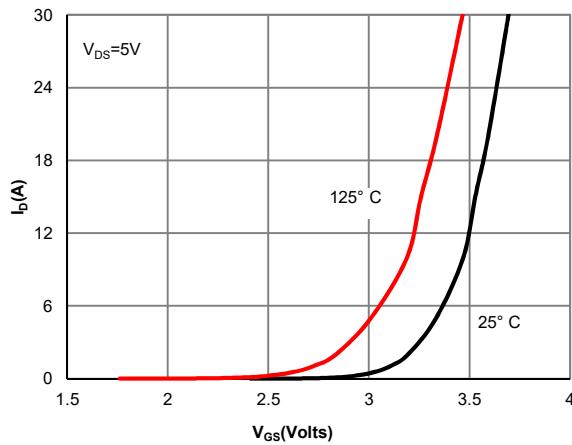


Figure 2: Transfer Characteristics

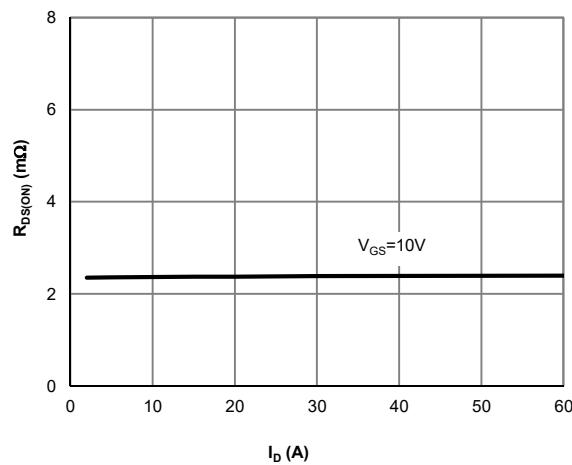


Figure 3:  $R_{DS(ON)}$  vs. Drain Current

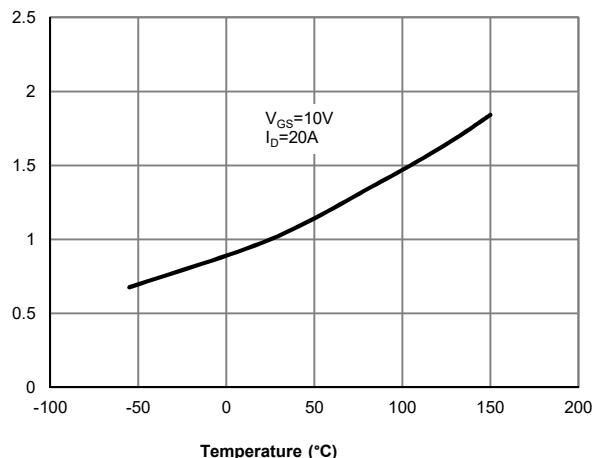


Figure 4:  $R_{DS(ON)}$  vs. Junction Temperature

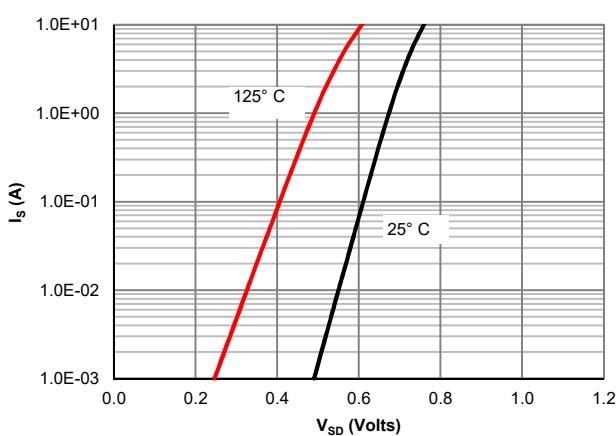


Figure 5: Body-Diode Characteristics

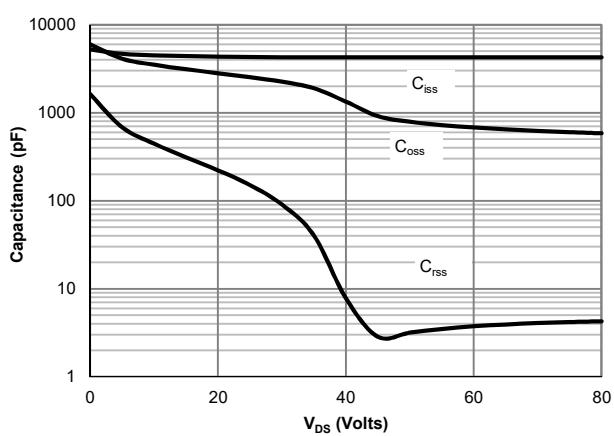


Figure 6: Capacitance Characteristics

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

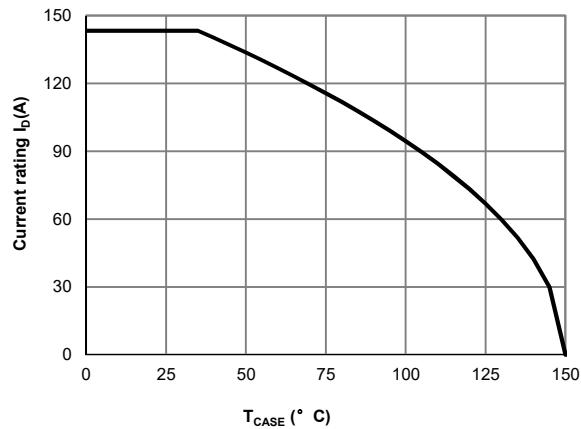


Figure 7: Current De-rating

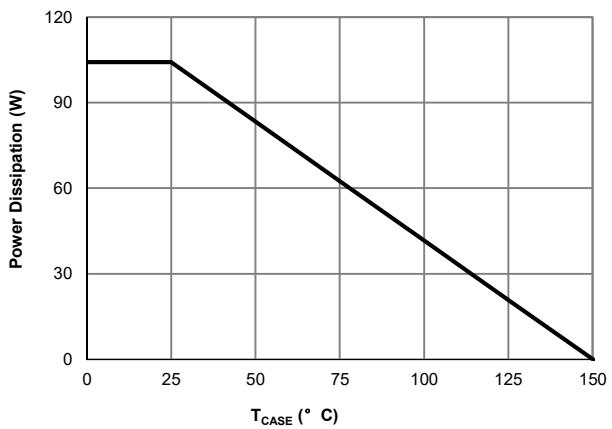


Figure 8: Power De-rating

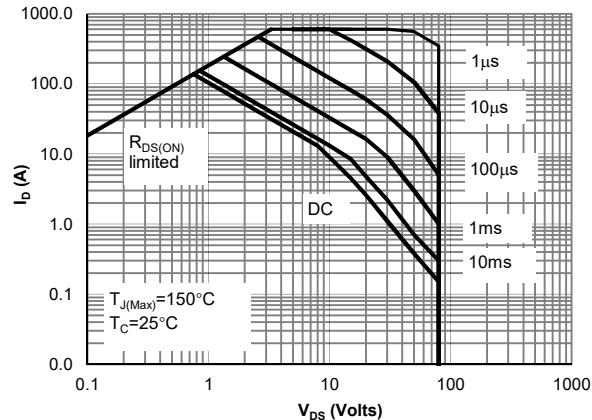


Figure 9: Maximum Safe Operating Area

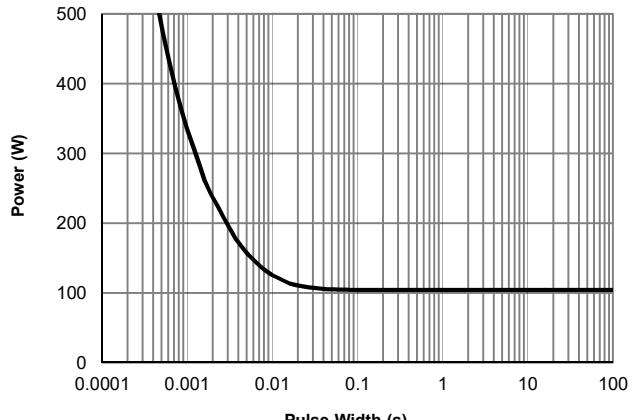


Figure 10: Single Pulse Power Rating Junction-to-Case

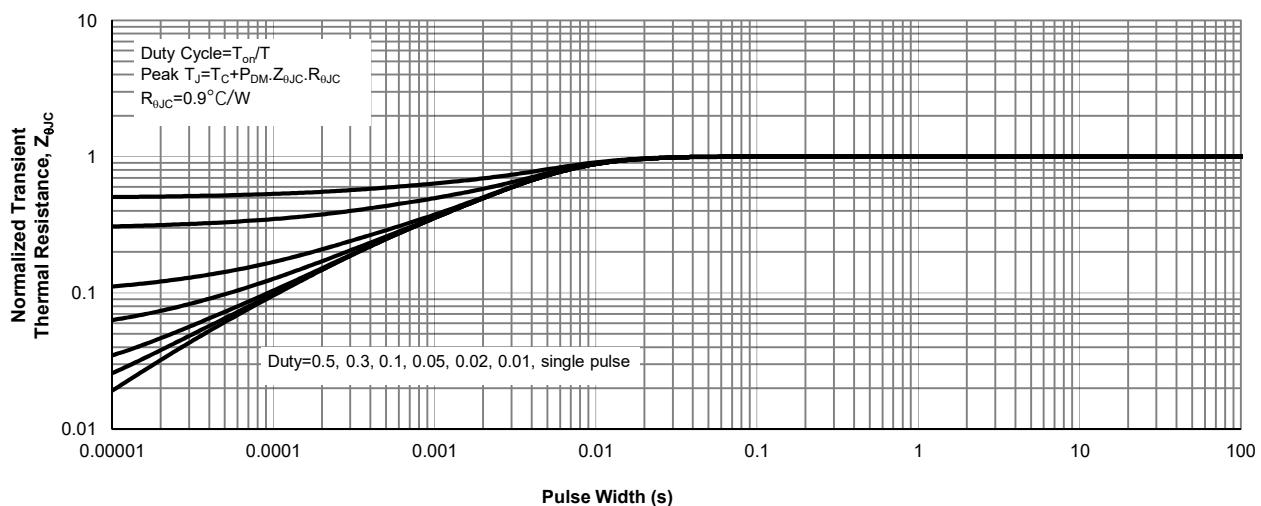
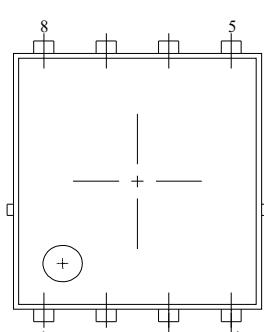
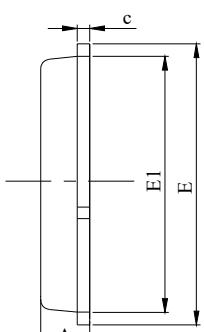


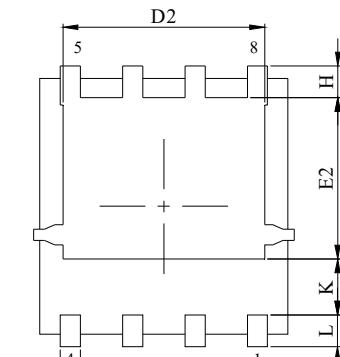
Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

**PDFN5x6-8L Package Information****Package Outline**

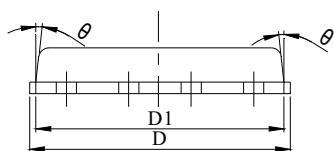
Top View



Side View



Bottom View

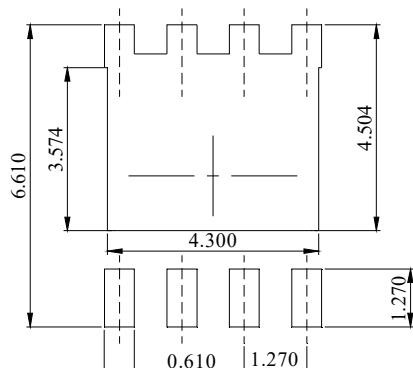


Front View

**NOTES:**

1. Dimension and tolerance per ASME Y14.5M, 1994.
2. All dimensions in millimeter (angle in degree).
3. Dimensions D1 and E1 do not include mold flash protrusions or gate burrs.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.31	0.41	0.51
c	0.20	0.25	0.30
D	5.00	5.20	5.40
D1	4.95	5.05	5.15
D2	4.00	4.10	4.20
E	6.05	6.15	6.25
E1	5.50	5.60	5.70
E2	3.42	3.53	3.63
e		1.27BSC	
H	0.60	0.70	0.80
L	0.50	0.70	0.80
K		1.23 REF	
$\theta$	-	-	10°

**Recommended Soldering Footprint**

DIMENSIONS: MILLIMETERS