



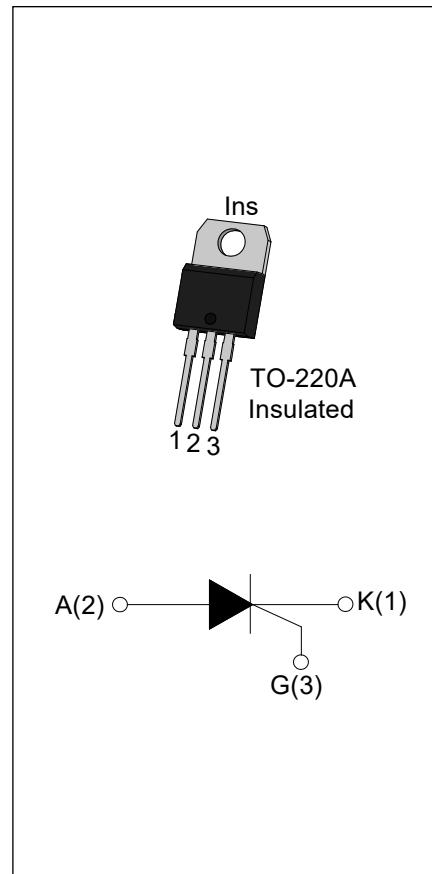
## JCT825A 25A SCRs

Rev.1

## DESCRIPTION:

With high ability to withstand the shock loading of large current, JCT825A SCRs provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.

From all three terminals to external heatsink, JCT825A provides a rated insulation voltage of 2500 V<sub>RMS</sub>, complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant. (2011/65/EU)



## MAIN FEATURES

Symbol	Value	Unit
V <sub>DRM</sub> / V <sub>RRM</sub>	800	V
I <sub>T(RMS)</sub>	25	A
I <sub>GT</sub>	≤30	mA

## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T <sub>stg</sub>	-40-150	°C
Operating junction temperature range	T <sub>j</sub>	-40-125	°C
Repetitive peak off-state voltage(T <sub>j</sub> =25°C)	V <sub>DRM</sub>	800	V
Repetitive peak reverse voltage(T <sub>j</sub> =25°C)	V <sub>RRM</sub>	800	V
RMS on-state current TO-220A(Ins) (T <sub>c</sub> =95°C)	I <sub>T(RMS)</sub>	25	A
Non repetitive surge peak on-state current (tp=10ms)	I <sub>TSM</sub>	300	A
I <sup>2</sup> t value for fusing (tp=10ms)	I <sup>2</sup> t	450	A <sup>2</sup> s
Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	dI/dt	50	A/μs

Peak gate current	I <sub>GM</sub>	4	A
Average gate power dissipation	P <sub>G(AV)</sub>	1	W
Peak gate power	P <sub>GM</sub>	5	W

**ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25°C unless otherwise specified)**

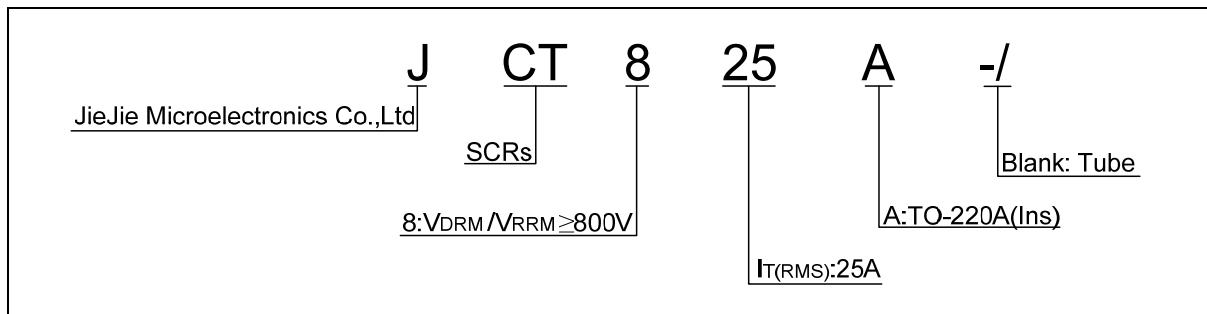
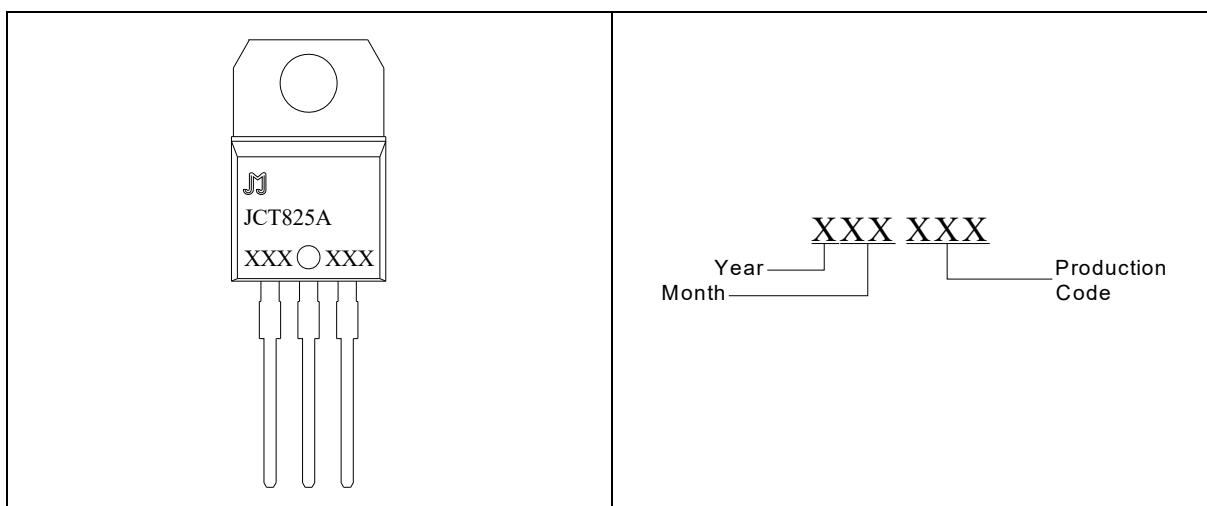
Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	-	-	30	mA
V <sub>GT</sub>		-	-	1.3	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C R <sub>L</sub> =3.3KΩ	0.2	-	-	V
I <sub>L</sub>	I <sub>G</sub> =1.2I <sub>GT</sub>	-	-	90	mA
I <sub>H</sub>	I <sub>T</sub> =500mA	-	-	80	mA
dv/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C	200	-	-	V/μs

**STATIC CHARACTERISTICS**

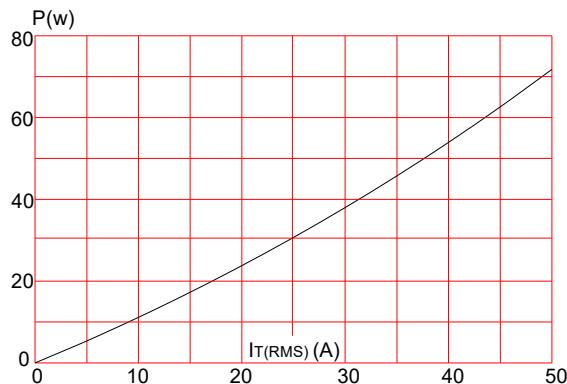
Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>TM</sub> =50A	t <sub>p</sub> =380μs	1.55	V
V <sub>TO</sub>	Threshold voltage	T <sub>j</sub> =150°C	0.94	V
R <sub>d</sub>	Dynamic resistance	T <sub>j</sub> =150°C	12	mΩ
I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25°C	10	μA
I <sub>RRM</sub>		T <sub>j</sub> =150°C	4	mA

**THERMAL RESISTANCES**

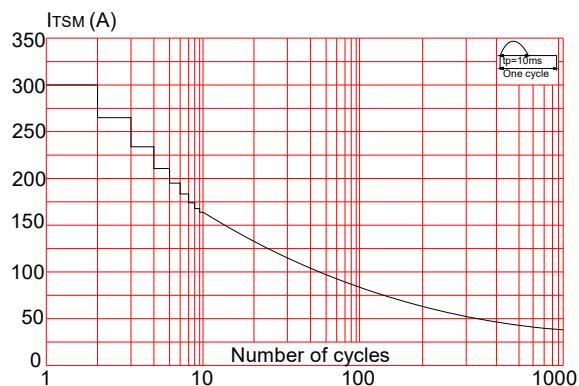
Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	junction to case(AC)	TO-220A(Ins)	1.7	°C/W

**ORDERING INFORMATION****MARKING**

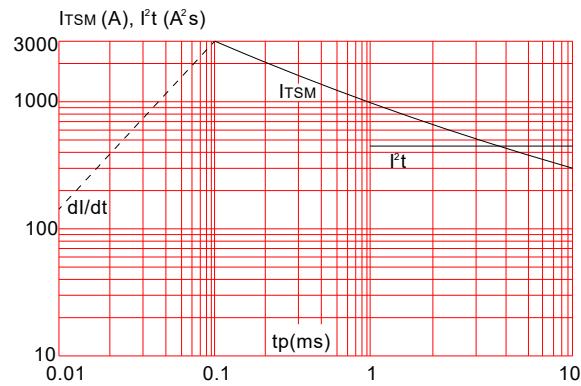
**FIG.1:** Maximum power dissipation versus RMS on-state current



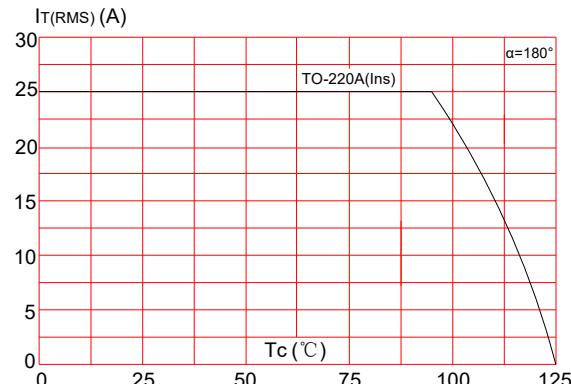
**FIG.3:** Surge peak on-state current versus number of cycles



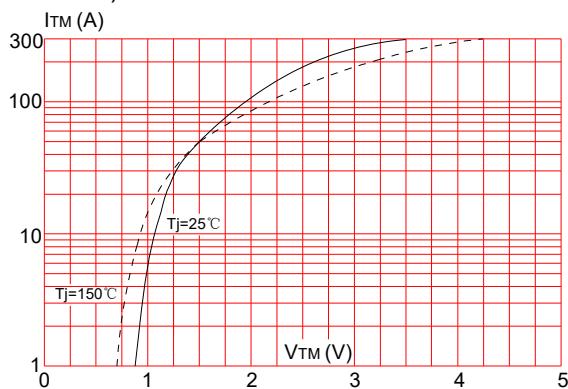
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10\text{ms}$ , and corresponding value of  $\dot{I}t$  ( $dI/dt < 50\text{A}/\mu\text{s}$ )



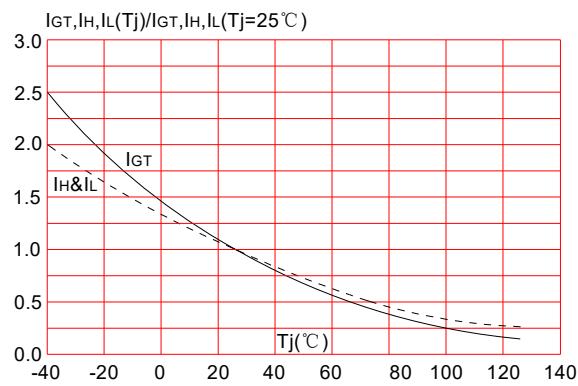
**FIG.2:** RMS on-state current versus case temperature



**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature



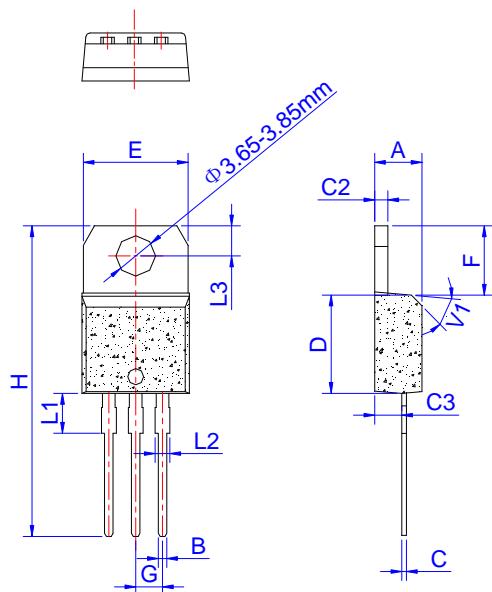
**ORDERING INFORMATION**

Order code	Voltage $V_{DRM}/V_{RRM}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT825A	800	30	TO-220A(Ins)	50	Tube

**Document Revision History**

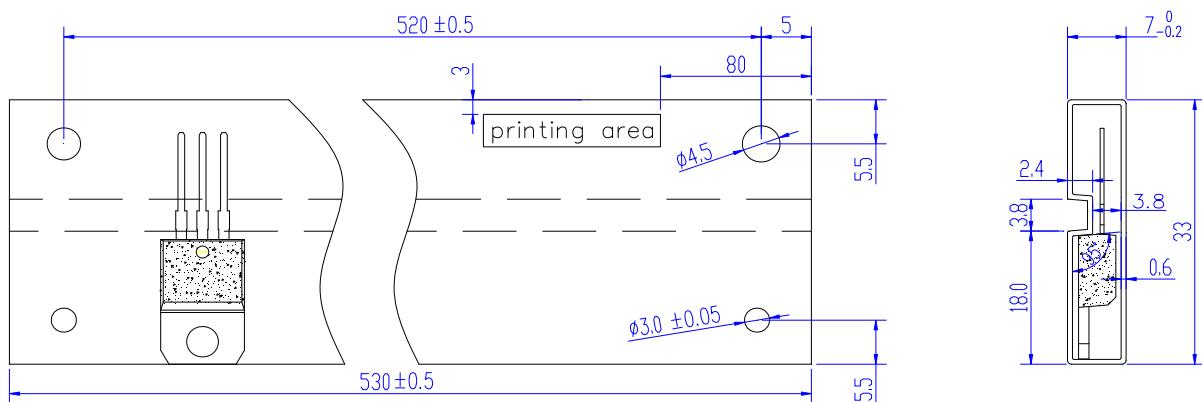
Date	Revision	Changes
Mar 18, 2022	1	Last update

## PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.80		10.4	0.386		0.409
F	6.55		6.95	0.258		0.274
G	2.40		2.70	0.094		0.106
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

## DELIVERY MODE



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220A	TUBE	50	1,000	5,000



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