

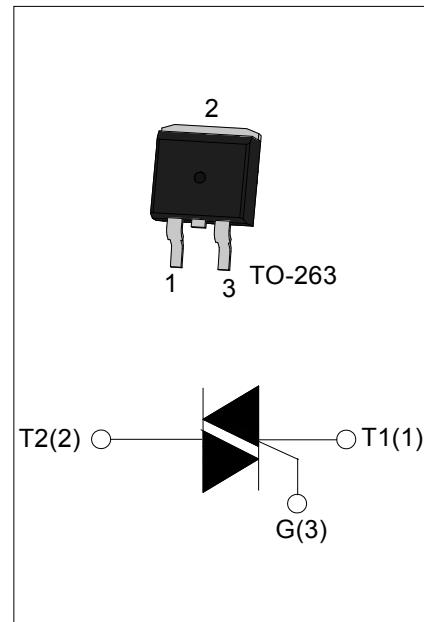


JST23H TO-263 25A TRIACs

Rev.1.0

DESCRIPTION:

With high dv/dt rate and strong resistance to electromagnetic interference, JST23H triacs of high junction temperature provide high ability to withstand the shock loading of large current. They are especially recommended for use on inductive load and high environment temperature condition. Package TO-263 is RoHS compliant. (2011/65/EU)



MAIN FEATURES

| Symbol | Value | Unit |
|---------------------|-------|------|
| I _{T(RMS)} | 25 | A |
| T _{jmax} | 150 | °C |

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Value | Unit |
|--|---------------------|------------|------------------|
| Storage junction temperature range | T _{stg} | -40 ~ +150 | °C |
| Operating junction temperature range | T _j | -40 ~ +150 | °C |
| Repetitive peak off-state voltage (T _j =25°C) | V _{DRM} | 600/800 | V |
| Repetitive peak reverse voltage (T _j =25°C) | V _{RRM} | 600/800 | V |
| RMS on-state current (T _c =110°C) | I _{T(RMS)} | 25 | A |
| Non repetitive surge peak on-state current (full cycle, F=50Hz) | I _{TSM} | 250 | A |
| I ² t value for fusing (tp=10ms) | I ² t | 310 | A ² s |
| Critical rate of rise of on-state current (I _G =2×I _{GT}) | dI/dt | 50 | A/μs |
| Peak gate current | I _{GM} | 4 | A |
| Average gate power dissipation | P _{G(AV)} | 1 | W |
| Peak gate power | P _{GM} | 5 | W |

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Test Condition | Quadrant | | Value | Unit |
|----------|--|--------------|-----|-------|------------|
| I_{GT} | $V_D = 12V$ $R_L = 33\Omega$ | I - II - III | MAX | 50 | mA |
| V_{GT} | | I - II - III | MAX | 1.3 | V |
| V_{GD} | $V_D = V_{DRM}$ $T_j = 150^\circ\text{C}$ | I - II - III | MIN | 0.2 | V |
| I_L | $I_G = 1.2I_{GT}$ | I - III | MAX | 70 | mA |
| | | II | | 100 | |
| I_H | $I_T = 1A$ | | MAX | 55 | mA |
| dV/dt | $V_D = 2/3V_{DRM}$ Gate Open $T_j = 150^\circ\text{C}$ | | MIN | 1000 | V/ μ s |

STATIC CHARACTERISTICS

| Symbol | Parameter | | Value(MAX) | Unit |
|-----------|-----------------|------------------------|---------------------------|------|
| V_{TM} | $I_{TM} = 35A$ | $t_p = 380\mu\text{s}$ | $T_j = 25^\circ\text{C}$ | 1.55 |
| I_{DRM} | $V_D = V_{DRM}$ | $V_R = V_{RRM}$ | $T_j = 25^\circ\text{C}$ | 10 |
| I_{RRM} | | | $T_j = 150^\circ\text{C}$ | 5 |

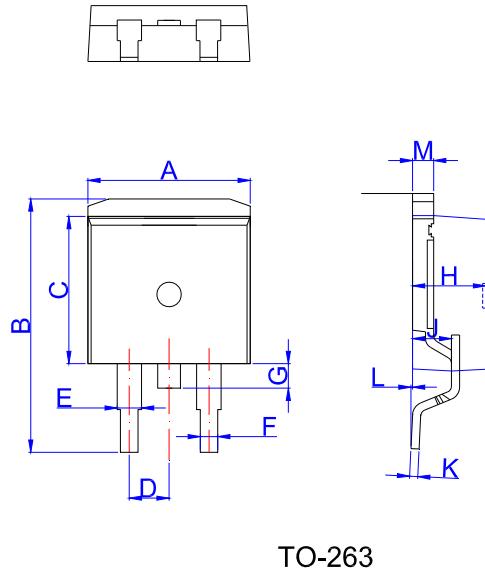
THERMAL RESISTANCES

| Symbol | Parameter | | Value | Unit |
|---------------|----------------------|--------|-------|------|
| $R_{th(j-c)}$ | junction to case(AC) | TO-263 | 0.97 | °C/W |
| $R_{th(j-a)}$ | junction to ambient | | 45 | |

ORDERING INFORMATION

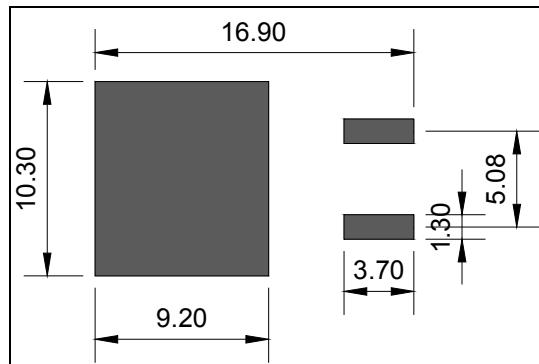
| | | | | | | |
|---------------------------------|--------|----|---|----|---|--|
| J | ST | 23 | H | -6 | E | E:TO-263 ETR:TO-263(Tape&Reel) |
| JieJie Microelectronics Co.,Ltd | Triacs | | | | | 6: $V_{DRM} \wedge V_{RRM} \geq 600V$ 8: $V_{DRM} \wedge V_{RRM} \geq 800V$ H: $T_j = 150^\circ\text{C}$ |

PACKAGE MECHANICAL DATA



| Ref. | Dimensions | | | | | |
|------|-------------|------|-------|--------|-------|-------|
| | Millimeters | | | Inches | | |
| | Min. | Typ. | Max. | Min. | Typ. | Max. |
| A | 9.90 | | 10.20 | 0.390 | | 0.402 |
| B | 14.70 | | 15.80 | 0.579 | | 0.622 |
| C | 9.4 | | 9.6 | 0.37 | | 0.378 |
| D | | 2.54 | | | 0.100 | |
| E | 1.20 | | 1.40 | 0.047 | | 0.055 |
| F | 0.75 | | 0.85 | 0.029 | | 0.033 |
| G | | | 1.75 | | | 0.069 |
| H | 4.40 | | 4.70 | 0.173 | | 0.185 |
| J | 2.30 | | 2.70 | 0.091 | | 0.106 |
| K | 0.38 | | 0.55 | 0.015 | | 0.022 |
| L | 0 | 0.10 | 0.25 | 0 | 0.004 | 0.010 |
| M | 1.25 | | 1.35 | 0.049 | | 0.053 |

FOOTPRINT-TO-263 (dimensions in mm)



PACKAGE INFORMATION

| PACKAGE | OUTLINE | TUBE (PCS) | INNER BOX (PCS) | PER CARTON |
|---------|---------|---------------|---------------------|-------------|
| TO-263 | TUBE | 50 | 1,000 | 6,000 |
| PACKAGE | OUTLINE | REEL (PCS) | PER CARTON (PCS) | TAPE & REEL |
| TO-263 | TAPING | 800 | 4,000 | 13 inch |



FIG.1: Maximum power dissipation versus RMS on-state current

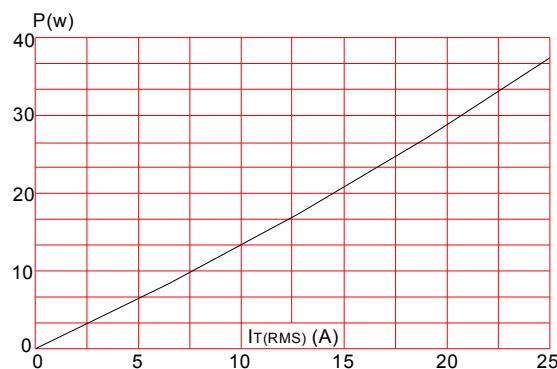


FIG.3: Surge peak on-state current versus number of cycles

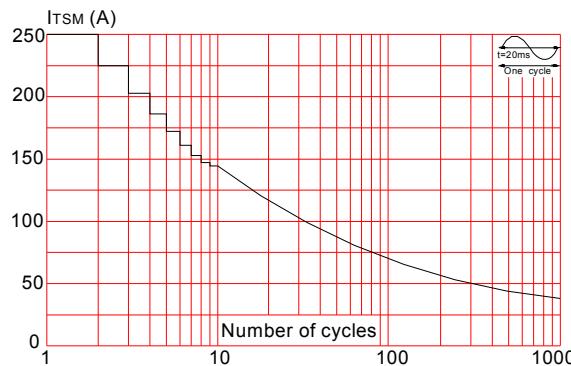


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($dI/dt < 50\text{A}/\mu\text{s}$)

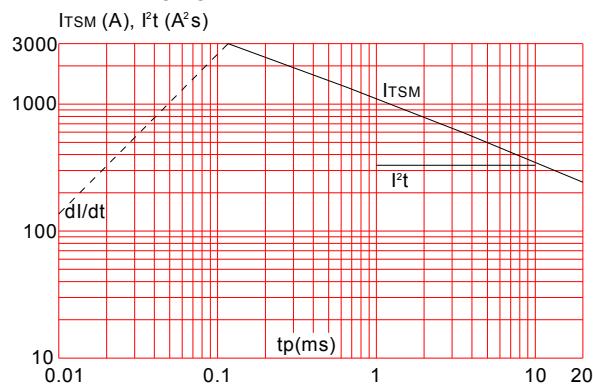


FIG.2: RMS on-state current versus ambient temperature (printed on circuit board FR4, copper thickness:35μm)(full cycle)

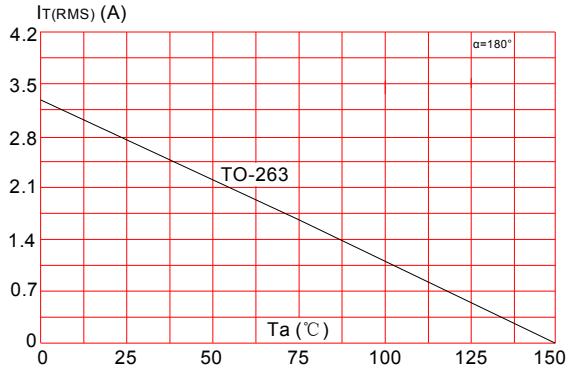


FIG.4: On-state characteristics (maximum values)

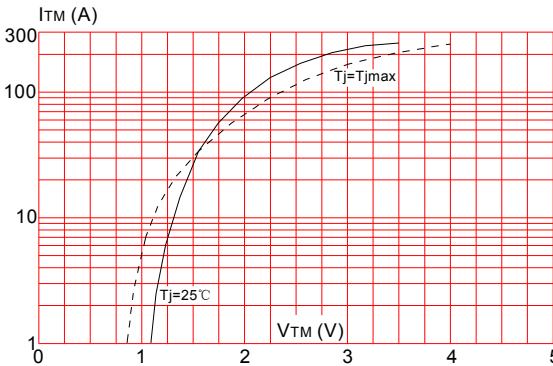
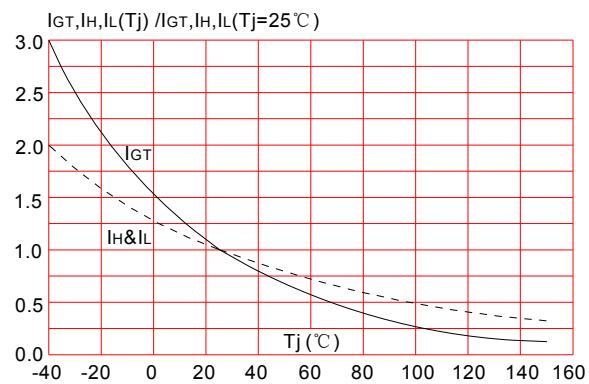
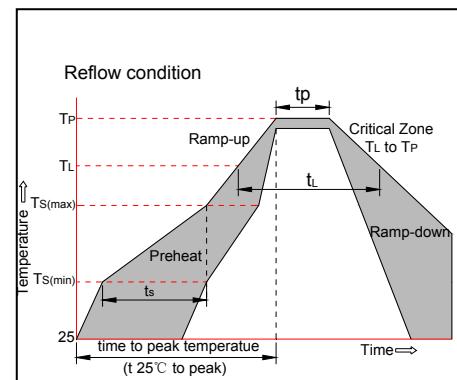


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



SOLDERING PARAMETERS

| | | |
|---|-------------------------------------|---|
| Reflow Condition | | Pb-Free assembly (see figure at right) |
| Pre Heat | -Temperature Min ($T_{s(\min)}$) | +150°C |
| | -Temperature Max($T_{s(\max)}$) | +200°C |
| | -Time (Min to Max) (ts) | 60-180 secs. |
| Average ramp up rate (Liquidus Temp (T_L) to peak) | | 3°C/sec. Max |
| $T_{s(\max)}$ to T_L - Ramp-up Rate | | 3°C/sec. Max |
| Reflow | -Temperature(T_L) (Liquidus) | +217°C |
| | -Temperature(t_L) | 60-150 secs. |
| Peak Temp (T_p) | | +260(+0/-5)°C |
| Time within 5°C of actual Peak Temp (t_p) | | 20-40secs. |
| Ramp-down Rate | | 6°C/sec. Max |
| Time 25°C to Peak Temp (T_p) | | 8 min. Max |
| Do not exceed | | +260°C |



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