



JX040 Series Sensitive gate SCRs

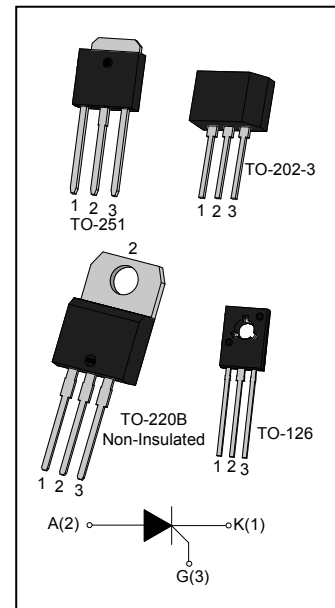
Rev.14.0

DESCRIPTION:

The JX040 SCR series provide high dv/dt rate with strong resistance to electromagnetic interface. They are especially recommended for use on straight hair, igniter etc. All the packages listed above are RoHS compliant. (2011/65/EU)

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
I_{GT}	≤ 200	μA



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T_{stg}	-40 - 150	$^{\circ}C$
Operating junction temperature range		T_j	-40 - 125 ^①	$^{\circ}C$
Repetitive peak off-state voltage		V_{DRM}	600	V
Repetitive peak reverse voltage		V_{RRM}	600	V
RMS on-state current	TO-126/ TO-202-3 ($T_C=85^{\circ}C$)	$I_{T(RMS)}$	4	A
	TO-251 ($T_C=90^{\circ}C$)			
	TO-220B(Non-Ins) ($T_C=105^{\circ}C$)			
Non repetitive surge peak on-state current (tp=10ms)		I_{TSM}	30	A
I^2t value for fusing (tp=10ms)		I^2t	4.5	A^2s
Critical rate of rise of on-state current		di/dt	50	$A/\mu s$
Peak gate current (tp=20 μs , $T_j=125^{\circ}C$)		I_{GM}	1.2	A
Peak gate power (tp=20 μs , $T_j=125^{\circ}C$)		P_{GM}	2	W
Average gate power dissipation($T_j=125^{\circ}C$)		$P_{G(AV)}$	0.2	W

NOTE 1: When we parallel connect a $\leq 1K\Omega$ resistor between Gate and Cathode, the T_j can reach $125^{\circ}C$; if without this resistor, the T_j only can reach $110^{\circ}C$.

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	$V_D=12\text{V } R_L=33\Omega$	-	50	200	μA
V_{GT}		-	0.6	0.8	V
V_{GD}	$V_D=V_{DRM} T_j=125^{\circ}\text{C}$	0.2	-	-	V
I_L	$I_G=1.2 I_{GT}$	-	-	6	mA
I_H	$I_T=0.05\text{A}$	-	-	5	mA
dV/dt	$V_D=2/3V_{DRM} T_j=125^{\circ}\text{C} R_{GK}=1\text{K}\Omega$	10	-	-	V/ μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V_{TM}	$I_T=8\text{A } t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.5	V
I_{DRM}	$V_D=V_{DRM} V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	100	μA

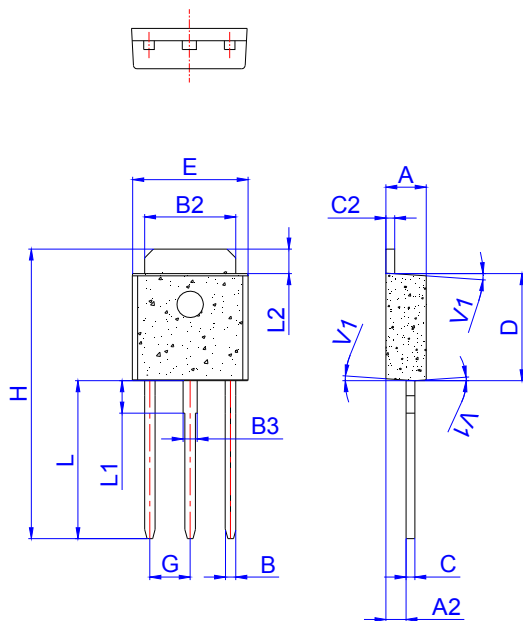
THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
$R_{th(j-c)}$	junction to case	TO-126	7.2	$^{\circ}\text{C/W}$
		TO-251	6.5	
		TO-220B(Non-Ins)	3.0	
		TO-202-3	7.6	

ORDERING INFORMATION

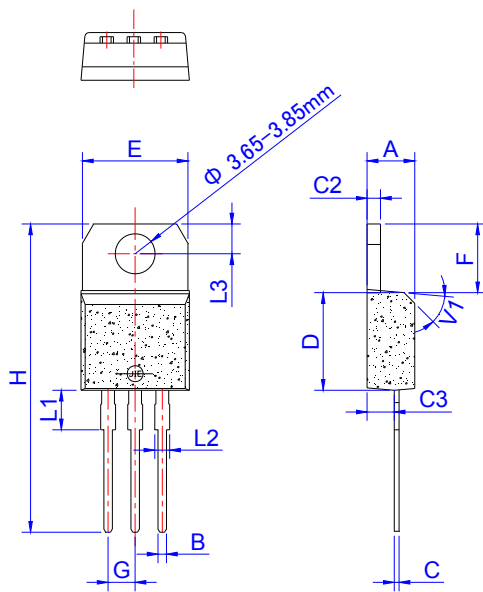
<p>J</p> <p>JieJie Microelectronics Co.,Ltd</p> <p>Sensitive gate SCRs</p>	<p>X</p> <p>$I_T(\text{RMS}):4\text{A}$</p>	<p>040</p>	<p>S3</p> <p>H:TO-251 Q:TO-126 S3:TO-202-3 B:TO-220B(Non-Ins)</p>
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PACKAGE MECHANICAL DATA



TO-251

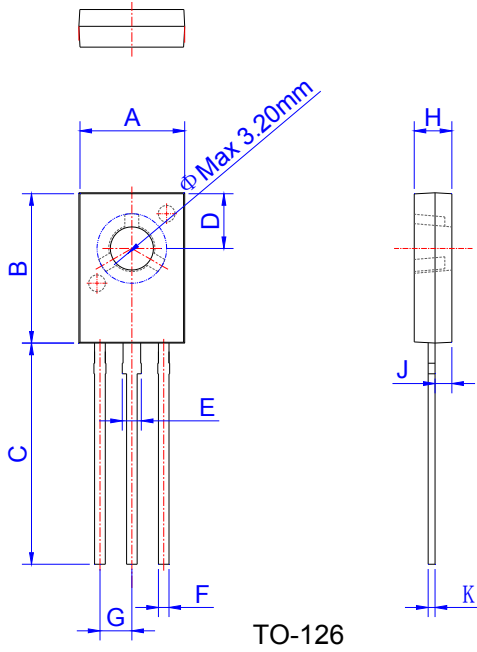
Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.90		1.20	0.035		0.047
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
B3	0.76		0.85	0.030		0.033
C	0.45		0.62	0.018		0.024
C2	0.48		0.62	0.019		0.024
D	6.00		6.20	0.236		0.244
E	6.40		6.70	0.252		0.264
G		2.30			0.091	
H	16.0		17.0	0.630		0.669
L	8.90		9.40	0.350		0.370
L1	1.80		1.90	0.071		0.075
L2	1.37		1.50	0.054		0.059
V1		4°			4°	



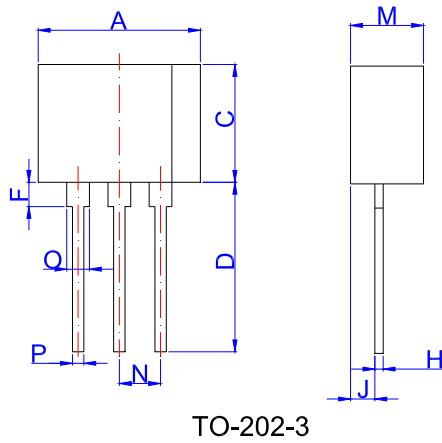
TO-220B (Non-Ins)

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.60		10.4	0.378		0.409
F	6.20		6.60	0.244		0.260
G	2.40		2.70	0.094		0.106
H	28.0		29.8	1.102		1.173
L1		3.75			0.147	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	7.40		7.80	0.291		0.307
B	10.6		11.2	0.417		0.441
C	15.3		16.3	0.602		0.642
D	3.90		4.10	0.154		0.161
E	1.17		1.47	0.046		0.058
F	0.66		0.86	0.026		0.034
G		2.29			0.090	
H	2.50		2.90	0.098		0.114
J	1.10		1.50	0.043		0.059
K	0.45		0.60	0.018		0.024



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.30		9.90	0.366		0.390
C	7.0		7.6	0.276		0.299
D	10.5		11.5	0.413		0.453
F	1.50		2.50	0.059		0.098
H	0.45		0.55	0.018		0.022
J	1.50		1.90	0.059		0.075
M	4.40		4.70	0.173		0.185
N		2.54			0.100	
O	1.20		1.50	0.047		0.059
P	0.60		0.80	0.024		0.031

MARKING

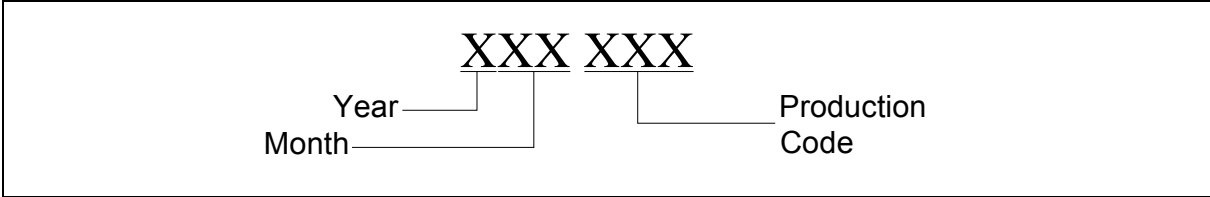
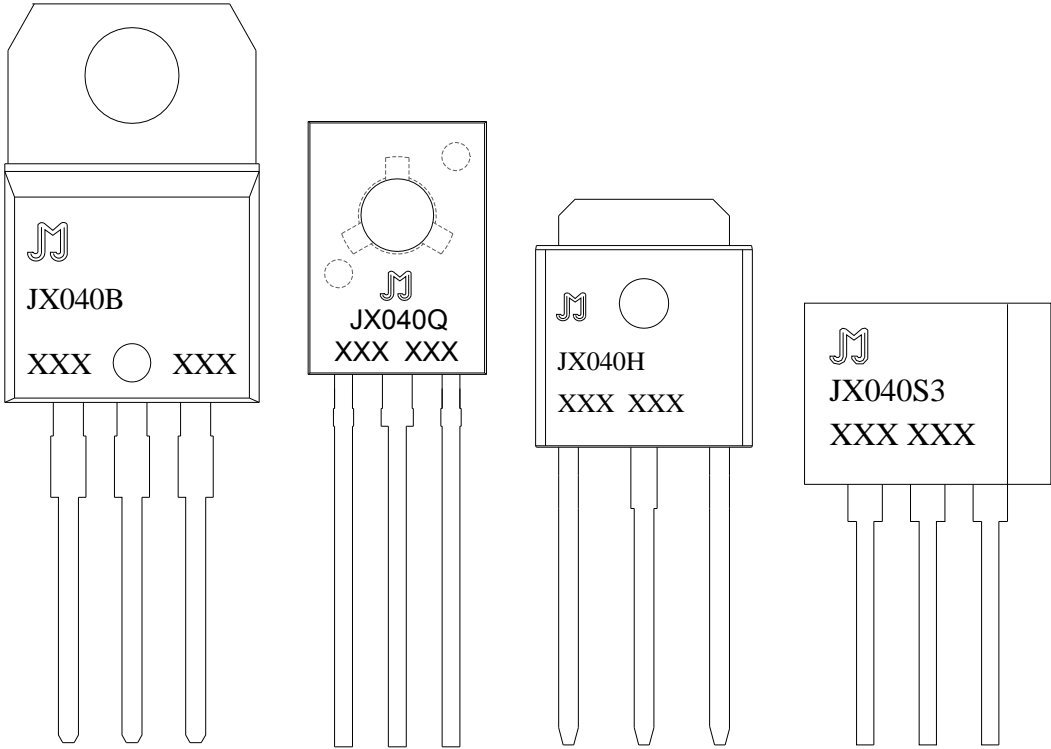


FIG.1: Maximum power dissipation versus RMS on-state current

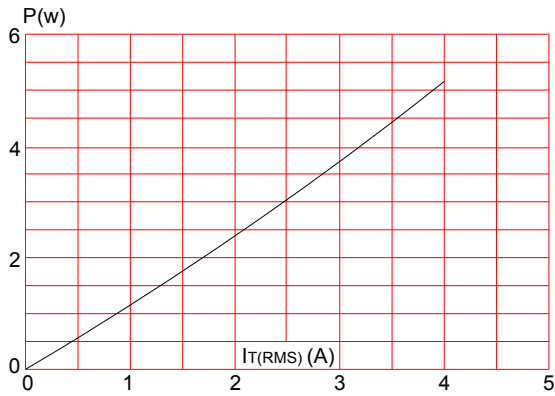


FIG.3: Surge peak on-state current versus number of cycles

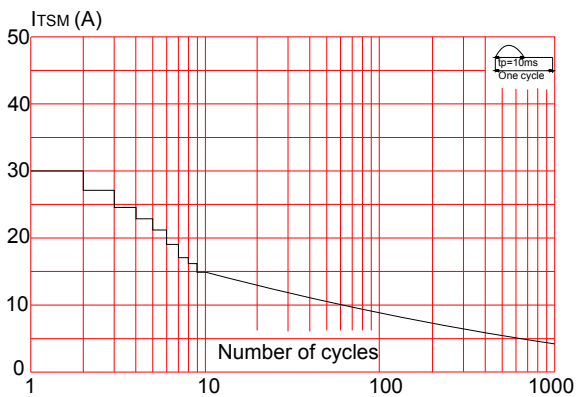


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$, and corresponding value of I^2t ($dI/dt < 50\text{A}/\mu\text{s}$)

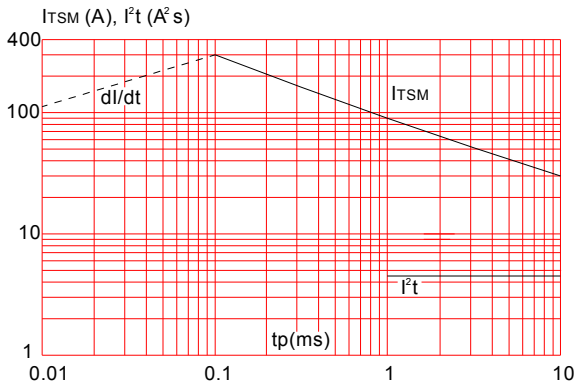


FIG.2: RMS on-state current versus case temperature

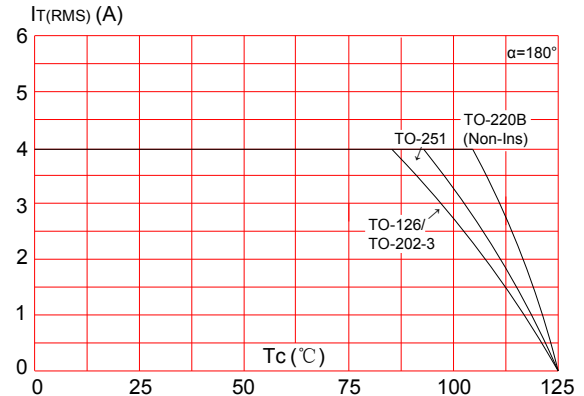


FIG.4: On-state characteristics (maximum values)

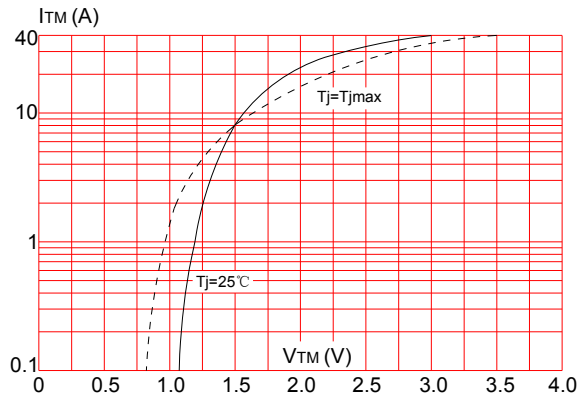
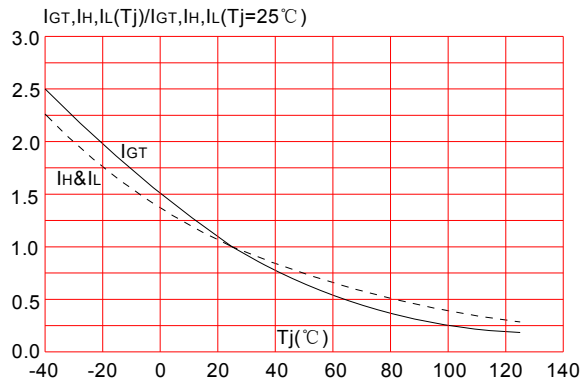



FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



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