

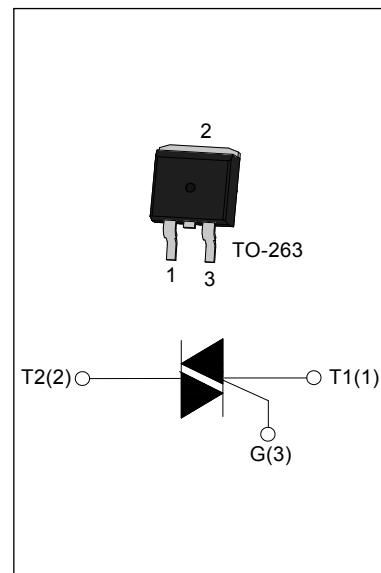


JST139E 16A TRIACs

Rev.8.0

DESCRIPTION:

JST139E triacs with low holding and latching current are especially recommended for use on middle and small resistance type power load.
Package TO-263 is RoHS compliant. (2011/65/EU)

**MAIN FEATURES**

Symbol	Value	Unit
$I_{T(RMS)}$	16	A
V_{DRM} / V_{RRM}	600/800	V

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-125	°C
Repetitive peak off-state voltage($T_j=25^\circ C$)	V_{DRM}	600/800	V
Repetitive peak reverse voltage($T_j=25^\circ C$)	V_{RRM}	600/800	V
RMS on-state current <small>($T_c=75^\circ C$)</small>	$I_{T(RMS)}$	16	A
Non repetitive surge peak on-state current <small>($t_p=20ms$)</small>	I_{TSM}	140	A
I^2t value for fusing ($t_p=10ms$)	I^2t	98	A^2s
Critical rate of rise of on-state current <small>($I_G=2 \times I_{GT}$)</small>	$I - II - III$ IV	50 10	$A/\mu s$
Peak gate current	I_{GM}	2	A
Average gate power dissipation	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	5	W

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant		Value				Unit
				D	E	F	B	
I_{GT}	$V_D=12V$ $R_L=33\Omega$	I - II - III	MAX	5	10	25	50	mA
		IV		10	25	70	70	
V_{GT}	ALL	MAX		1.3				V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^\circ\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN	0.2				V
I_L	$I_G=1.2I_{GT}$	I - III	MAX	15	30	50	80	mA
		II - IV		20	40	100	120	
I_H	$I_T=100\text{mA}$	MAX		10	25	40	60	mA
dV/dt	$V_D=2/3V_{DRM}$ Gate Open $T_j=125^\circ\text{C}$	MIN		20	50	100	500	V/ μ s

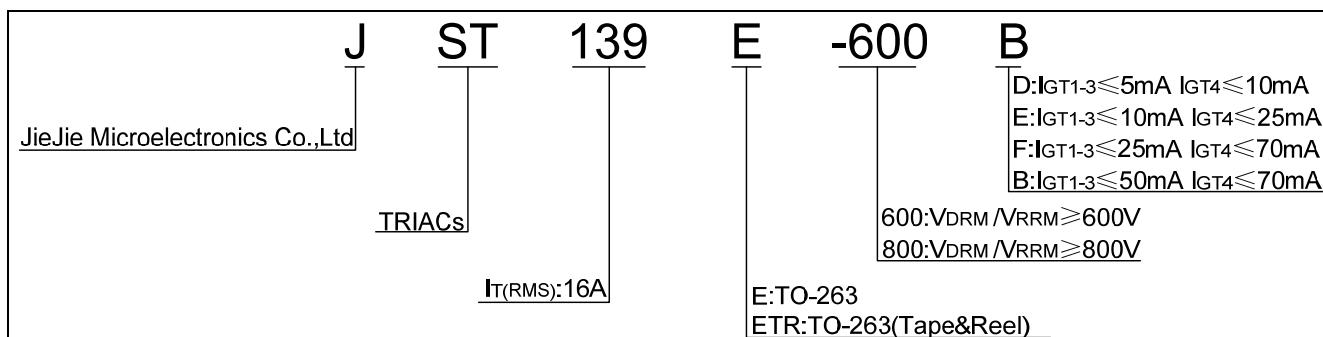
STATIC CHARACTERISTICS

Symbol	Parameter	Value(MAX)	Unit
V_{TM}	$I_{TM}=20\text{A}$ $t_p=380\mu\text{s}$	1.6	V
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^\circ\text{C}$	μA
I_{RRM}		$T_j=125^\circ\text{C}$	1 mA

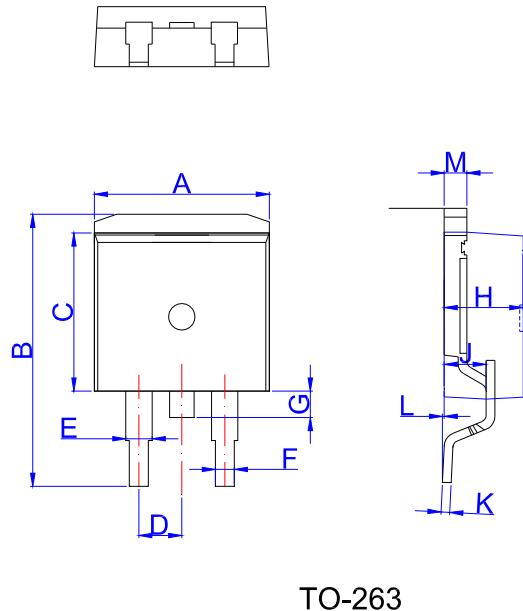
THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case(AC)	1.2	$^\circ\text{C}/\text{W}$

ORDERING INFORMATION

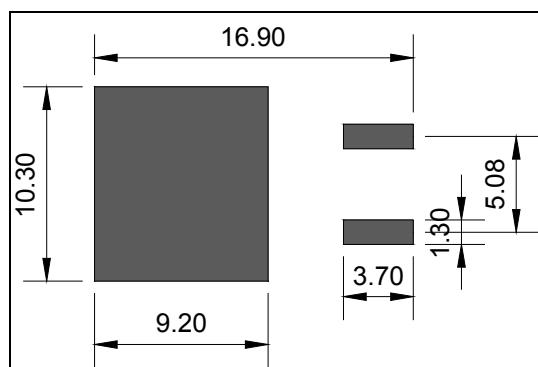


PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
B	14.70		15.80	0.579		0.622
C	9.4		9.6	0.37		0.378
D		2.54			0.100	
E	1.20		1.40	0.047		0.055
F	0.75		0.85	0.029		0.033
G			1.75			0.069
H	4.40		4.70	0.173		0.185
J	2.30		2.70	0.091		0.106
K	0.38		0.55	0.015		0.022
L	0	0.10	0.25	0	0.004	0.010
M	1.25		1.35	0.049		0.053

FOOTPRINT-TO-263 (dimensions in mm)



PACKAGE INFORMATION

PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-263	TUBE	50	1,000	5,000
PACKAGE	OUTLINE	REEL (PCS)	PER CARTON (PCS)	TAPE & REEL
TO-263	TAPING	800	4,000	13 inch

FIG.1 Maximum power dissipation versus RMS on-state current

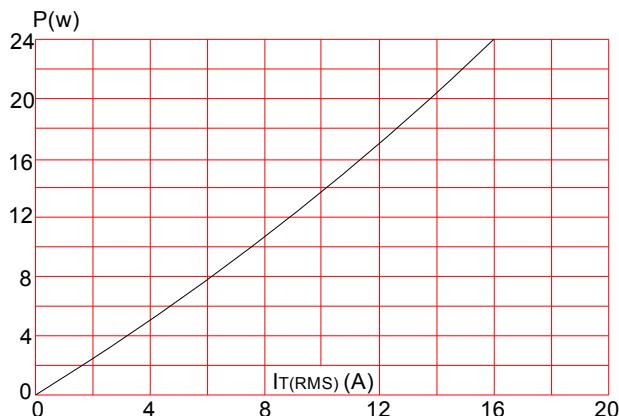


FIG.3: Surge peak on-state current versus number of cycles

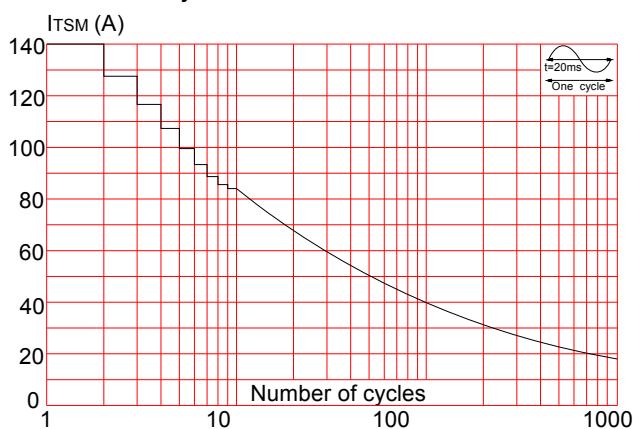


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$ and corresponding value of I^2t (I - II - III: $dl/dt < 50\text{A}/\mu\text{s}$; IV: $dl/dt < 10\text{A}/\mu\text{s}$)

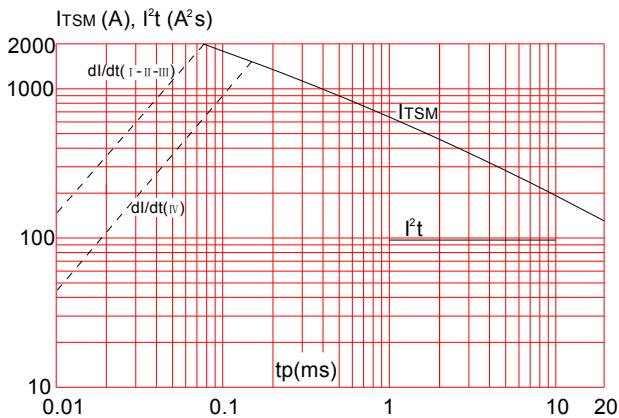


FIG.2: RMS on-state current versus case temperature

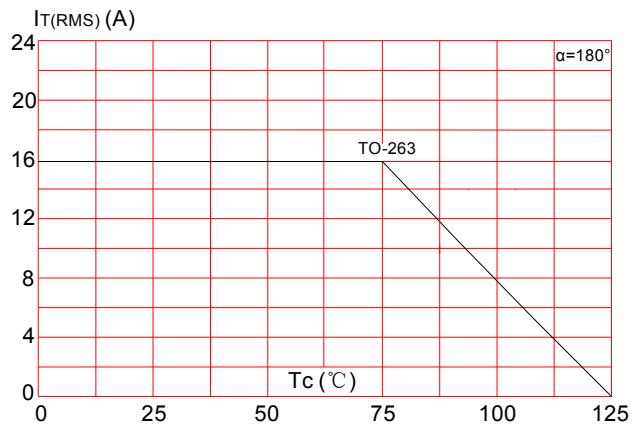


FIG.4: On-state characteristics (maximum values)

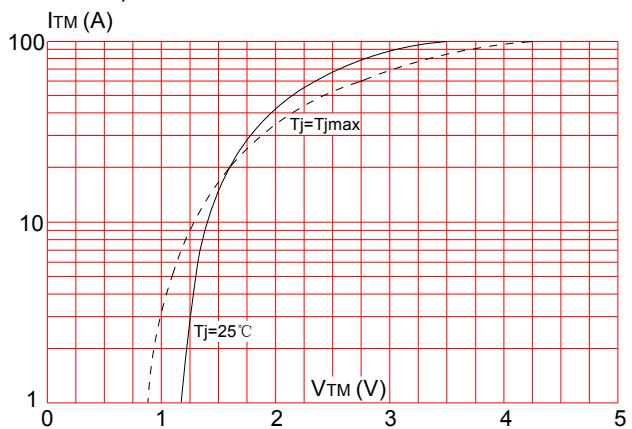


FIG.6: Relative variations of gate trigger current versus junction temperature

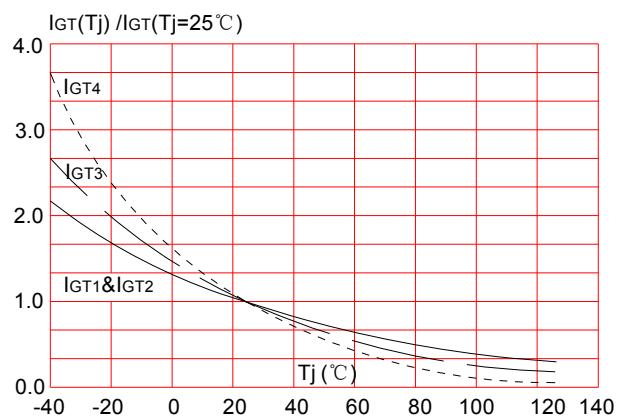


FIG.7: Relative variations of holding current versus junction temperature

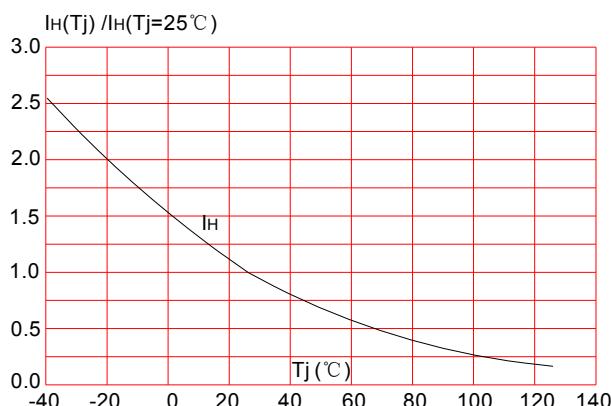
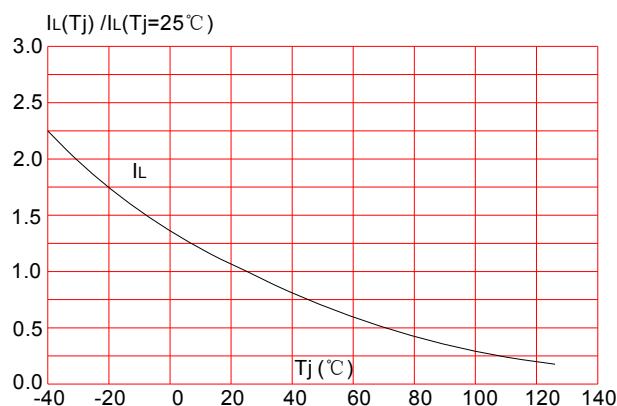


FIG.8: Relative variations of latching current versus junction temperature



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the eighth version which is made in 23-July-2020. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright ©2020 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.