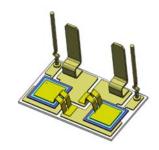


Rev.1.2 Oct.20 2023

# DBC056C/xxKQ-KGxC

# **Description**

- 1) Components adopt vacuum welding to well control void and rated voltage up to 1600V.
- 2) A package of two inverse parallel SCRs.
- 3) Thyristor chips are welding on the ceramic copper clad laminate, products with high electricity ability, excellent heat dissipation ability.



# **Typical Application**

Constant temperature system, CNC machine, remote control system, lighting control, power compensation and so on.

### **Absolute Maximum Ratings** (Packaged into modules, unless otherwise specified, T<sub>CASE</sub>=25℃)

	- 10 III		Values		
Parameter	Test Conditions	Symbol	12	16	Unit
Operating junction temperature range		TJ	-40~	+125	$^{\circ}\!\mathbb{C}$
Repetitive peak off-state voltage	TJ=25℃	V <sub>DRM</sub>	1200	1600	٧
Repetitive peak reverse voltage	TJ=25℃	V <sub>RRM</sub>	1200	1600	٧
Non-repetitive peak off-state voltage	TJ=25℃	V <sub>DSM</sub>	1300	1700	V
Non-repetitive peak reverse voltage	TJ=25℃	V <sub>RSM</sub>	1300	1700	V
Average on-state current	Tc=80℃	I <sub>T(AV)</sub>	5	6	А
RMS on-state current	Tc=80℃	I <sub>T(RMS)</sub>	9	0	А
Non-repetitive surge peak on-state current	t⊳=10ms	Ітѕм	11	20	Α
I <sup>2</sup> t value for fusing	t <sub>P</sub> =10ms	l <sup>2</sup> t	62	00	A <sup>2</sup> s
Critical rate of rise of on-state current	IG=2×IGT	di/dt	1	50	A/µs

#### **Electrical Characteristics** (Packaged into modules, unless otherwise specified, T<sub>CASE</sub>=25°C)

Parameter	Test Conditions	Symbol	Values	Unit
Peak on-state voltage	I <sub>TM</sub> =168A,t <sub>P</sub> =380μs	V <sub>TM</sub>	≤1.8	V
	V <sub>D</sub> =V <sub>DRM</sub>			
Repetitive peak off-state current	Tc=25℃	I <sub>DRM1</sub>	≤50	μΑ
	Tc=125℃	I <sub>DRM2</sub>	≤10	mA

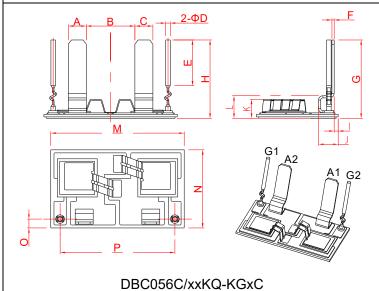


# **Solid DBC Modules**

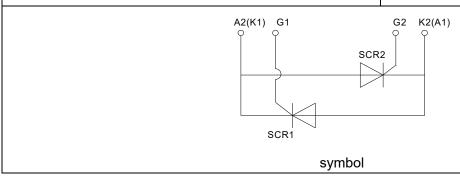
	V <sub>R</sub> =V <sub>RRM</sub>			
Repetitive peak reverse current	Tc=25℃	I <sub>RRM1</sub>	≤50	μΑ
	Tc=125℃	I <sub>RRM2</sub>	≤10	mA
Triggering gate current	V <sub>D</sub> =12V R <sub>L</sub> =30Ω	lgт	10-80	mA
Latching current	Ig=1.2 Igт	lι	≤200	mA
Holding current	Iτ=1A	Ін	≤150	mA
Triggering gate voltage	V <sub>D</sub> =12V R <sub>L</sub> =30Ω	V <sub>GT</sub>	≤1.5	V
Non triggering gate voltage	V <sub>D</sub> =V <sub>DRM</sub> T <sub>J</sub> =125℃	$V_{GD}$	≥0.25	V
Critical rate of rise of voltage	V <sub>D</sub> =2/3V <sub>DRM</sub> T <sub>J</sub> =125℃ Gate Open	dv/dt	≥1000	V/µs

# **Mechanical Characteristics**

Chip size	8.9mm×8.9mm
Module size	29.7mm×18.2mm
Terminal height	19.2mm
Solder composition and melting point of DBC	Solder composition: Pb92.5%Sn5%Ag2.5%; melting point>295℃.



	Dimensions						
Ref	Ref Milli		illimeters		Inches		
	Min	Тур	Max	Min	Тур	Max	
Α	3.7	4.0	4.3	0.146	0.157	0.169	
В	10.3	10.8	11.3	0.406	0.425	0.445	
С	3.7	4.0	4.3	0.146	0.157	0.169	
D		1.0			0.039		
Е		10.65			0.419		
F	0.3	0.5	0.7	0.012	0.020	0.028	
G			19.2			0.756	
Н			19.2			0.756	
1	0.4	0.9	1.4	0.016	0.035	0.055	
J	3.9	4.4	4.9	0.154	0.173	0.193	
K			6.0			0.236	
L			6.2			0.244	
М	29.4	29.7	30	1.157	1.169	1.181	
N	17.9	18.2	18.5	0.705	0.717	0.728	
0	1.6	2.1	2.6	0.063	0.083	0.102	
Р	25.1	25.6	26.1	0.988	1.008	1.028	

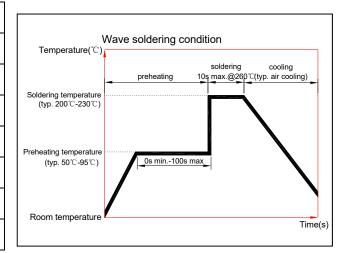






### **Soldering Process Requirements**

- construing i recess requirements			
a. Hand soldering iron welding			
Soldering temperature	≤260℃		
Soldering time	≤10s		
b. Wave soldering (see figure at right)			
Preheating temperature	≤125℃		
Preheating time	≤100s		
Soldering temperature	≤260℃		
Soldering time	≤10s		



#### **Working Conditions**

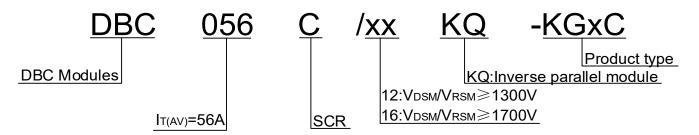
1) No severe mechanical shock as impact and drop off in the process of transportation, storage and working of product.

2) Storage conditions
Temperature: 5~40°C

Relative humidity: ≤45%

Storage time: 3 days for the open package; 3 months for the closed package

### **Ordering Information**



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